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**Term:**

model with ((disk or disc) drive) with  
characteristics

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USPT model with ((disk or disc) drive) with characteristics

14

L1

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 IBM Technical Disclosure Bulletins

**Term:**

(processor with (digital adj5 convertor)) same  
 (semiconductor) and (reduc\$3 adj3 cost)

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<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT	(processor with (digital adj5 convertor)) same (semiconductor) and (reduc\$3 adj3 cost)	5	<u>L6</u>
USPT	(processor with (digital convertor)) same (single chip) same (reduc\$3 adj3 cost)	0	<u>L5</u>
USPT	(plural\$3 adj3 circuit) same (single chip) same (reduc\$3 adj3 cost)	5	<u>L4</u>
USPT	(pluaral\$3 adj3 circuits) same (single chip) same (reduc\$3 adj3 cost)	0	<u>L3</u>
USPT	(pluaral\$3 adj3 circuits) with (single chip) same (reduc\$3 adj3 cost)	0	<u>L2</u>
USPT	(pluaral\$3 adj3 circuits) with (single chip) with (reduc\$3 adj3 cost)	0	<u>L1</u>

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT	l14 and l12	4	<u>L15</u>
USPT	feed forward	4754	<u>L14</u>
USPT	l2 and l12	0	<u>L13</u>
USPT	l9 and l10 and l11	23	<u>L12</u>
USPT	expected with acceleration	911	<u>L11</u>
USPT	expected with velocity	2167	<u>L10</u>
USPT	expected with position	8439	<u>L9</u>
USPT	model same l6	4	<u>L8</u>
USPT	l2 and l6	0	<u>L7</u>
USPT	expected with (position with velocity with acceleration)	12	<u>L6</u>
USPT	l3 and l1	3	<u>L5</u>
USPT	(l2 same l3) and l1	0	<u>L4</u>
USPT	(feed forward) with expected	29	<u>L3</u>
USPT	dsp or (digital signal processor)	17774	<u>L2</u>
USPT	(disk or disc) adj3 (drive or apparatus)	52772	<u>L1</u>